Rad Hard Non Volatile Memory for FPGA BootLoading [1]

Submitted by drupal on Thu, 10/23/2014 - 13:43

Firm:
Space Micro, Inc. [2]
Award Solicitation:
NASA SBIR 2012 Phase II Solicitation [3]
Award ID:
SBIR_12_P2_124140
Award Topic:
Radiation Hardened/Tolerant and Low Temperature Electronics and Processors [4]
Award Dollars:
743,926.00
Award Lead Center:
Marshall Space Flight Center [5]
Proposal Number:
H6.02-9860
Proposal Title:
Rad Hard Non Volatile Memory for FPGA BootLoading
Mission Directorate:
Human Exploration and Operations [6]
EHB Solicitation ID:
SBIR_12_P2
Selection:
NASA 2012 SBIR Program Phase II Selections [7]
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